

Appl. No. 10/530,266
Amdt. Dated September 29, 2006
Reply to Office Action of June 29, 2006

Listing of Claims:

1. (currently amended) Integrated circuit comprising a plurality of processing modules (M, S) said modules being disposed on the same chip, and a network (N; RN) arranged for providing at least one connection between a first and at least one second module (M, S),

wherein said modules communicate via a network on chip, and

wherein said connection supports transactions comprising outgoing messages from the first module to the second modules and return messages from the second modules to the first module, the integrated circuit comprising at least one dropping means (DM) for dropping data exchanged by said first and second module (M, S).

2. (original) Integrated circuit according to claim 1, further comprising: at least one interface means (ANIP, PNIP) for managing the interface between a module (M, S) and the network (N, RN),

wherein said interface means (ANIP, PNIP) comprises a first dropping means (DM) for dropping data.

3. (original) Integrated circuit according to claim 2, wherein said network (N, RN) comprises a plurality of network routers for forwarding data without dropping data.

4. (original) Integrated circuit according to claim 2 or 3, wherein said dropping means (DM) are adapted to create an error message if data is dropped.

5. (original) Integrated circuit according to claim 4, wherein said dropping means (DM) are adapted to send error

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message to a first dropping means (DM).

6. (original) Integrated circuit according to claim 4, wherein said dropping means (DM) are adapted to send said error message to said first module (M).

7. (original) Integrated circuit according to claim 5 or 6, wherein

said interface means (ANIP, PNIP) is adapted to store received error messages.

8. (original) Integrated circuit according to claim 7, wherein an interface means (ANIP) associated to the first module (M) is adapted not to drop error messages.

9. (currently amended) Method for exchanging messages in an integrated circuit comprising a plurality of modules, wherein said modules communicate via a network on chip, the messages between the modules being exchanged over connections via a network,

wherein said connection supports transactions comprising outgoing messages from the first module to the second module and return messages from the second module to the first module, wherein data exchanged by said first and second module (M,S) can be dropped.